

RESPONSE UNDER 37 C.F.R. 1.116
EXPEDITED PROCEDURE
EXAMINING GROUP 2186

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of

Chang et al. (SANDP040)

Serial No. 10/678,893

Filed: October 2, 2003

Conf. No. 2329

Group Art Unit: 2186

Examiner: Tsai

For: Hybrid Implementation for Error Correction Codes Within a Non-Volatile Memory System

AMENDMENT UNDER RULE 116

Commissioner for Patents

P.O. Box 1450

Alexandria, VA 22313-1450

Dear Sir:

please enter.

Thanks.

SOI

01/05/2007

Introductory Comments:

This paper is presented in response to the Office Action mailed on September 19, 2006. Applicants submit that entry of this amendment will place all claims in condition for allowance, or alternatively will place this application in better condition for appeal. Entry of this amendment in this application, and favorable reconsideration of this application based on that amendment and these accompanying remarks, are respectfully requested.

Amendment to the claims begins on page 2.

Applicants' remarks begin on page 9.